

ABSTRACT OF THE DISCLOSURE

A1

A single chip type microcomputer includes at least a central processing unit (CPU), a random-access memory (RAM), a mask read-only memory (CPU) and an electrically writable ROM such as an electrically erasable and programmable read-only memory (EEP-ROM). The electrically writable ROM stores both the user program and data to be preserved. The microcomputer includes further a memory for storing a write control program for controlling the write operation to the writable ROM, and the electrically writable ROM and the memory are disposed at mutually different address positions on the address space of CPU. The proportion of size of the user program region and the data region in the writable ROM can be selected in a free proportion.

IN THE SPECIFICATION:

A2

At page 1, as the first sentence following the title, please insert [This application is a continuation of U.S. Serial No. 07/344,455 filed on April 27, 1989 (pending) of Naoki Mitsubishi which is a file wrapper continuing application of U.S. Serial No. 07/029,750 filed March 24, 1987 of Naoki Mitsubishi (now abandoned);

Page 5, line 22, change "microcomputer" to --microcomputer--;

Page 6, line 4, change "write control circuit 7" to --write control unit 7 (Fig. 2)--;

line 28, delete "EEP-ROM";

Page 9, line 27, change "EEP-ROM 3" to --EEP-PROM 4--;

line 28, change "communcation" to --communication--;

Page 11, after line 28, add -- See discussion on Fig. 5, above.--